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22918 7590 01/12/2006

PERKINS COIE LLP
 P.O. BOX 2168
 MENLO PARK, CA 94026

04/19/2006 RHEBRAH1 00000038 09989647

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Sherry Castro	(Depositor's name)
<i>[Signature]</i>	(Signature)
April 12, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,647	11/20/2001	Gyudong Kim	594728122US	1014

TITLE OF INVENTION: LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	04/12/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
JOSEPH, JAISON	2634	375-219000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Perkins Coie LLP
 2 _____
 3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Silicon Image

Sunnyvale, CA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☒ Issue Fee
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☒ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number 50-2207 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

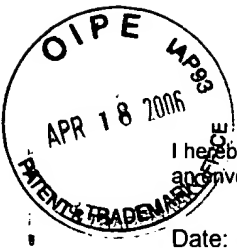
☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature *[Signature]*Date 4/12/06Typed or printed name James R. HannahRegistration No. 56,369

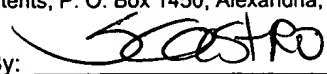
This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Date: April 12, 2006

By: 

Sherry Castro

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

KIM, ET AL.

APPLICATION NO.: 09/989,647

FILED: NOVEMBER 20, 2001

FOR: **LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS**

EXAMINER: JOSEPH, JAISON

ART UNIT: 2634

CONF. NO. 1014

ATTORNEY DOCKET NO.: 59472-8822.US00

NOTICE OF ALLOWANCE: JANUARY 12, 2006

Transmittal of Issue Fee, Publication Fee and Advance Order

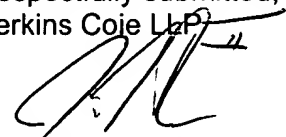
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Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance dated January 12, 2006, applicants herewith submit the following:

- ☒ Form PTOL-85B (in duplicate)
- ☒ Check in the amount of \$1,400.00 for:
 - 1) Issue Fee (\$1,400.00) – Large Entity
- ☒ Please charge any additional fees necessary for consideration of this paper to Deposit Account No. 50-2207.

Respectfully submitted,
Perkins Coie LLP



James R. Hannah
Registration No. 56,369

Date: April 12, 2006

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